

ABSTRACT

A protected dual purpose power/enter switch for an integrated receiver/decoder has a first relay and a second relay adapted for operative communication with a motherboard and with each other. A switch and a processor are in operative communication with the first relay. When the device is off, said switch is configured to activate the first and second relay to power up the device, and when said device is on, the first relay connects the switch to the processor as an enter switch. The switch may power off said device by entering a power off menu item displayed by said processor.